

SANYO Semiconductors DATA SHEET



Monolithic Linear IC **LA42205** — Audio Output for TV application 5W × 2ch Power Amplifier

Overview

The LA42205 is a 5W 2-channel power amplifier IC and optimal for use as the audio output power amplifier in TV application

Functions

- 5W \times 2 channels (V_{CC} = 9.5V, R_L = 8 Ω)
- Built-in standby function.
- Built-in mute function.
- Built-in various protection circuit (short to VCC/short to ground/load shorting/overheating).

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

-				
Parameter Syml		Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	No signal	16	V
Allowable power dissipation	Pd max	Infinitely large heat sink	30	W
Maximum output current	I _O peak	Per channel	1.0	А
Maximum junction temperature	Tj max		150	°C
Thermal resistance	θјс		2.5	°C/W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		9.5	V
Recommended load resistance	RL		8	Ω
Allowable operating supply	V _{CC} op	Under conditions such that	7 to 14	V
voltage range		maximum ratings are not exceeded		

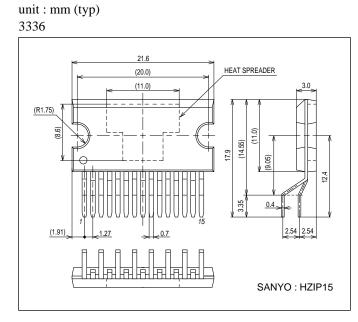
- Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.
- Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

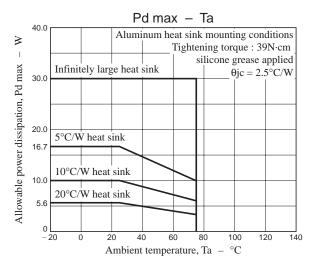
SANYO Semiconductor Co., Ltd. www.semiconductor-sanyo.com/network

Parameter	Symbol	Conditions		Ratings				
Parameter	Symbol	Conditions	min	typ	max	Unit		
Standby current	Ist	Amplifier OFF		0	10	μA		
Quiescent current	Icco	$Rg = 0, R_L = OPEN$	35	70	140	mA		
Output power	P _O 1	THD = 10%	4	5		W		
Total harmonic distortion	THD	P _O = 1W		0.06	0.2	%		
Voltage gain	VG	V _O = 0dBm	30	32	34	dB		
Output noise voltage	V _{NO}	Rg = 0, BPF = 20Hz to 20kHz		0.2	0.4	mVrms		
Ripple rejection ratio	SVRR	$Rg = 0$, $fR = 100Hz$, $V_{CC}R = 0dBm$	40	50		dB		
Channel separation	CH.Sep	$Rg = 10k\Omega$, $V_O = 0dBm$	50	60		dB		
Mute attenuation value	ATT	V_{O} = 1Vrms, BPF = 20Hz to 20kHz	80	90		dB		
Mute control voltage (pin 7) Vmute		Mute ON *1	1.5		3.0	V		
		Mute OFF	0		0.5	V		
Standby control voltage (pin 6) Vst		Amplifier ON *1	2.5		V _{CC}	V		
		Amplifier OFF	0		0.5	V		
Input resistance	Ri		21	30	39	kΩ		

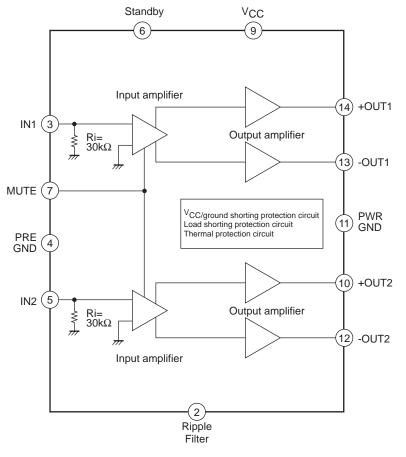
*1 : Note that the standby pin (pin 6) and MUTE pin (pin 7) incorporate the anti-electrostatic diode allowing the current to flow through the diode when the potential of V_{CC} (pin 9) decreases below that of pin 6 or 7.

Package Dimensions

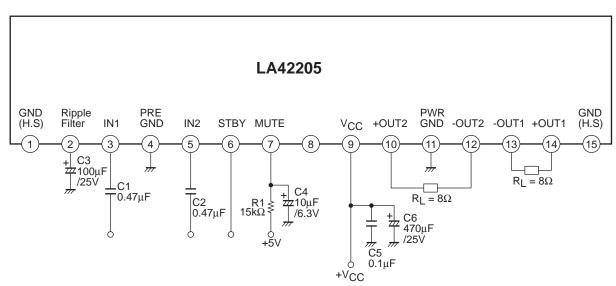








Test Circuit



* Pin 8 : NC. This pin must be left open.

* Pins 1 and 15 are connected to the heat sink. They must be connected to power and ground.

Pin Voltage

Conditions : $V_{CC} = 9.5V$, STBY = 5V

Pin	n No.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
P	Pin	GND	Ripple	IN1	Pre	IN2	STBY	Mute	N.C.	VCC	+OUT2	Power	-OUT2	-OUT1	+OUT1	GND
		(H.S)	Filter		GND							GND				(H.S)
	oltage V)	0	9.2	0.7	0	0.7	5	0	-	9.5	4.8	0	4.8	4.8	4.8	0

External Components

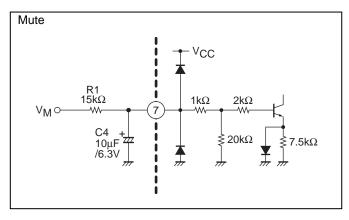
C1, C2	: Input coupling capacitors. A value of 0.47µF is recommended.
	The input pin potential is 0.7V.
C3	: Capacitor which sets the starting time of the ripple filter and amplifier. A value of 100µF is recommended.
C4, R1	: Capacitor and resistor for muting function. C4 is necessary even when the mute function is not used.
C5	: This capacitor increases the oscillator margin. We recommend a value of 0.1µF.
C6	: Power supply capacitor.

1. Muting function (pin 7)

The muting function is turned on when the pin 7 voltage is 1.5V (minimum) or higher.

The V_M applied voltage is set so that the pin 7 voltage will be 1.5V or higher.

The muting time constant is determined by an C4, R1 circuit, and the component values must be determined by a careful analysis, since they are related to impulse noise that occurs when the muting function is turned on or off. Also note that since C4 affects the occurrence of impulse noise when the amplifier is turned on or off, it will be required if the muting function is not used.



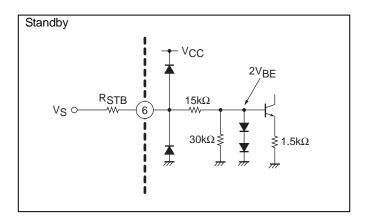
2. Standby function (pin 6)

The amplifier is turned on by applying a level of 2.5V (minimum) or higher to pin 6.

Pin 6 c	control	voltage
---------	---------	---------

Pin 6 voltage	Amplifier	Standby		
0 to 0.5	Off	On		
2.5 to V _{CC}	On	Off		

The limit resistor R_{STB} may be inserted if the V_{STB} applied voltage is comparatively high and there is a need to suppress the pin 6 sink current.

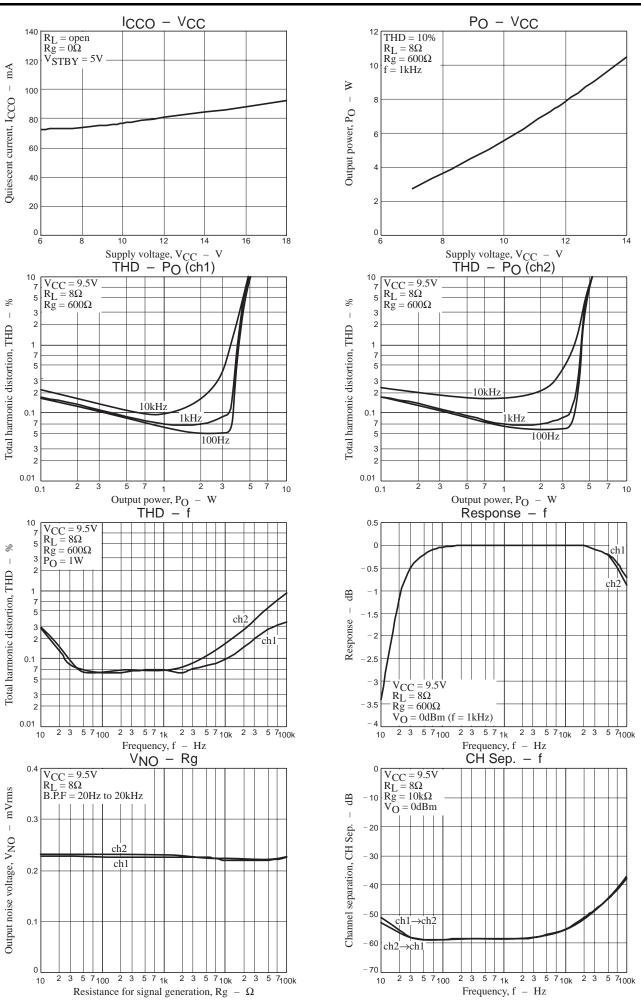


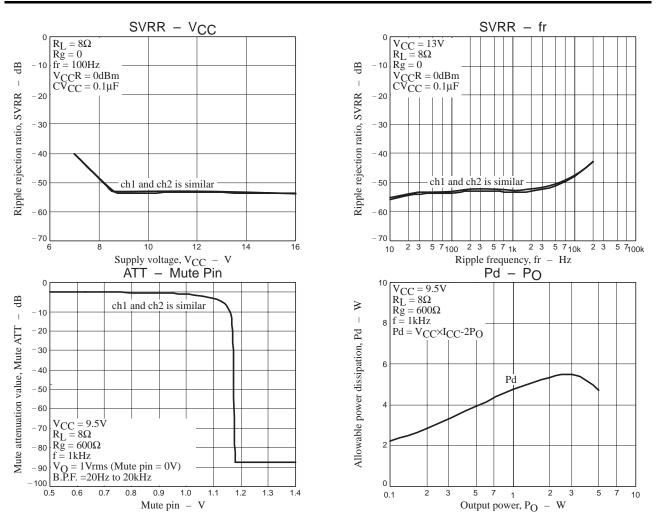
Usage Notes

 This IC includes three short circuit protection circuits : shorting to power (shorts between an output and the power supply), shorting to ground (shorts between an output and ground), and load shorting protection circuits. These protection circuits operate while the corresponding abnormal condition continues, and recover automatically when the abnormal condition is resolved.

There are, however, cases where, due to IC usage conditions, one of these protection circuits may lock and continue operating after the problem has been resolved. If this occurs, the protection circuit state can be reset by either switching the IC to standby mode or by temporarily cutting power from the IC.

- 2. This IC includes a thermal protection circuit that operates if the junction temperature (Tj) rises to 160°C or higher. This circuit gradually reduces the output level.
- 3. If this IC is operated in the vicinity of any of its maximum ratings, even slight changes in operating conditions could result in a maximum rating being exceeded. Since this could lead to the destruction of the IC, end product designs must include adequate margins in the supply voltage and other parameters so that the IC is always used within ranges such that the maximum ratings are never exceeded.





- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of December, 2009. Specifications and information herein are subject to change without notice.