TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSIII)

# 2SK2847

#### DC-DC Converter and Motor Drive Applications

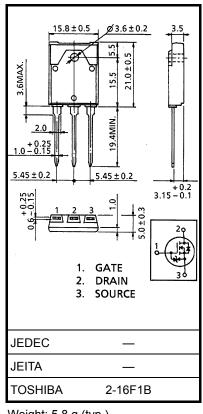
• Low drain-source ON resistance  $: RDS (ON) = 1.1 \Omega (typ.)$ 

• High forward transfer admittance  $|Y_{fs}| = 7.0 \text{ S (typ.)}$ 

Absolute Maximum Ratings (Ta = 25°C)

- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 720 \ V)$
- Enhancement mode :  $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$

#### Characteristics Symbol Rating Unit Drain-source voltage VDSS 900 V V Drain-gate voltage (R<sub>GS</sub> = 20 kΩ) 900 VDGR V Gate-source voltage V<sub>GSS</sub> ±30 DC (Note 1) 8 А $I_D$ Drain current Pulse (Note 1) 24 А $I_{DP}$ Drain power dissipation (Tc = 25°C) $P_D$ 85 w Single pulse avalanche energy 799 mJ EAS (Note 2) Avalanche current 8 IAR Α Repetitive avalanche energy (Note 3) EAR 8.5 mJ °C Channel temperature T<sub>ch</sub> 150 Storage temperature range -55~150 °C T<sub>stg</sub>



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Мах	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	1.47	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	41.6	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 22.9 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 8 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution. Unit: mm

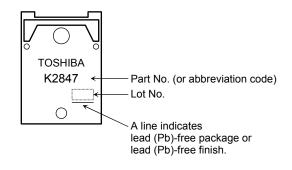
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	—	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V			100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900		_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A		1.1	1.4	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.0	7.0	_	S
Input capacitance	ce	C <sub>iss</sub>			2040	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		45	_	
Output capacitance		Coss			190	_	
Switching time	Rise time	tr	$V_{\rm GS} \stackrel{10V}{}_{0V} \qquad I_{\rm D} = 4A \\ V_{\rm OUT} \qquad V_{\rm OUT} \\ 4.7\Omega \\ 4.7\Omega \\ m \\ m \\ m \\ 0 \\ V_{\rm DD} $	_	25	_	- ns
	Turn-on time	t <sub>on</sub>		_	60	_	
	Fall time	t <sub>f</sub>		_	20	_	
	Turn-off time	t <sub>off</sub>	$\Rightarrow$ 400V Duty $\leq$ 1%, t <sub>w</sub> =10 $\mu$ s		95	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	58	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A		32	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>			26	_	

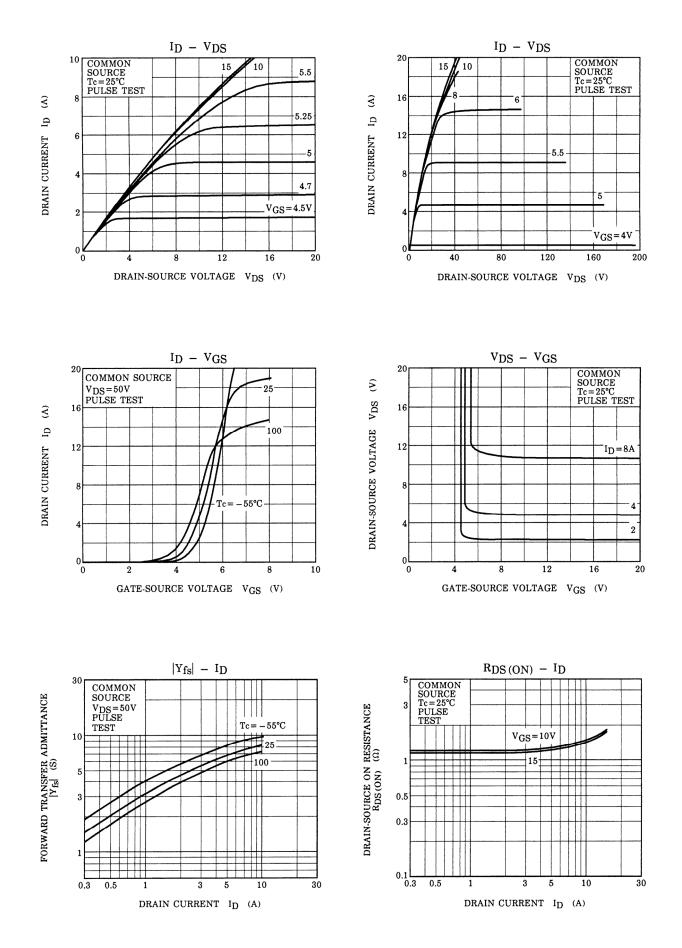
### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	8	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—			24	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V			-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 8 A, V <sub>GS</sub> = 0 V		1650		ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / µs	_	21	_	μC

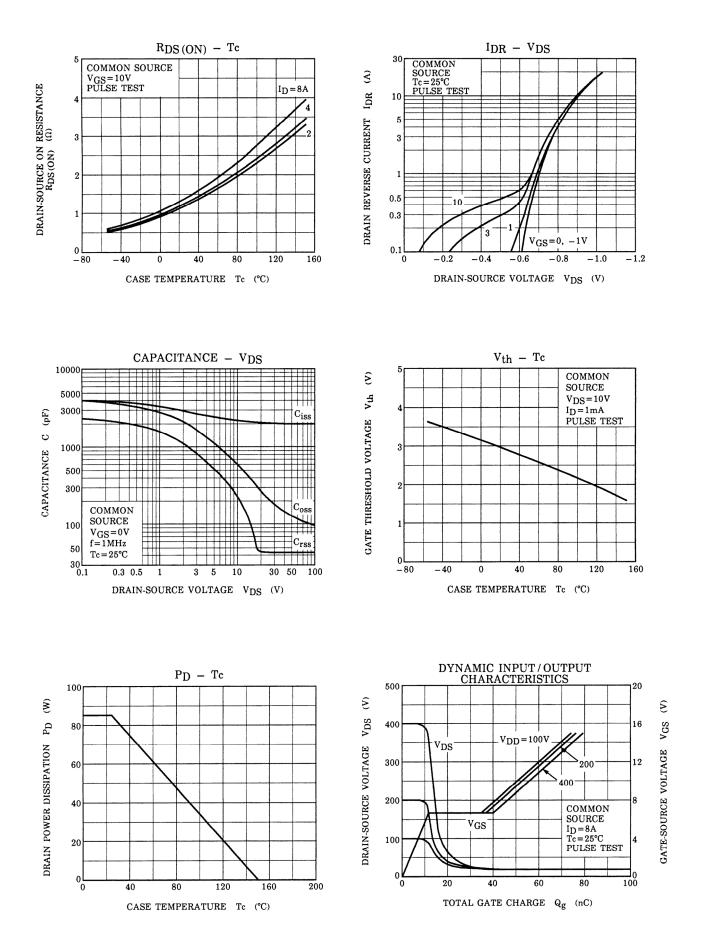
### Marking



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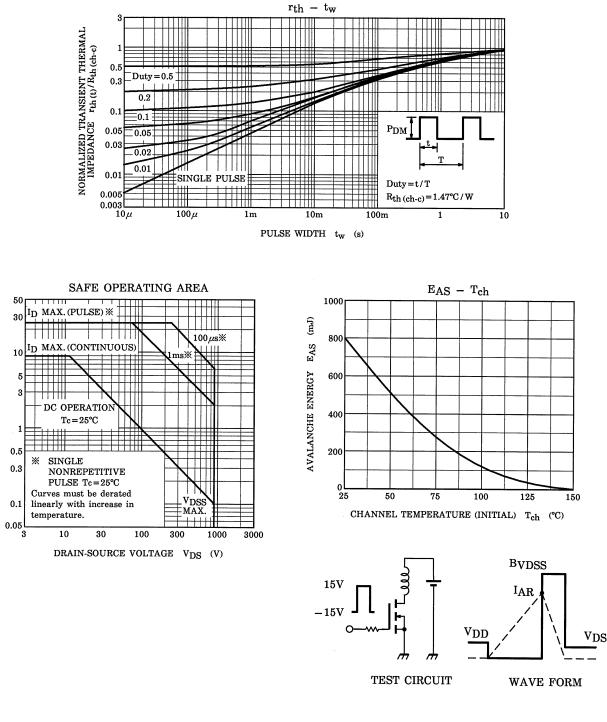
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